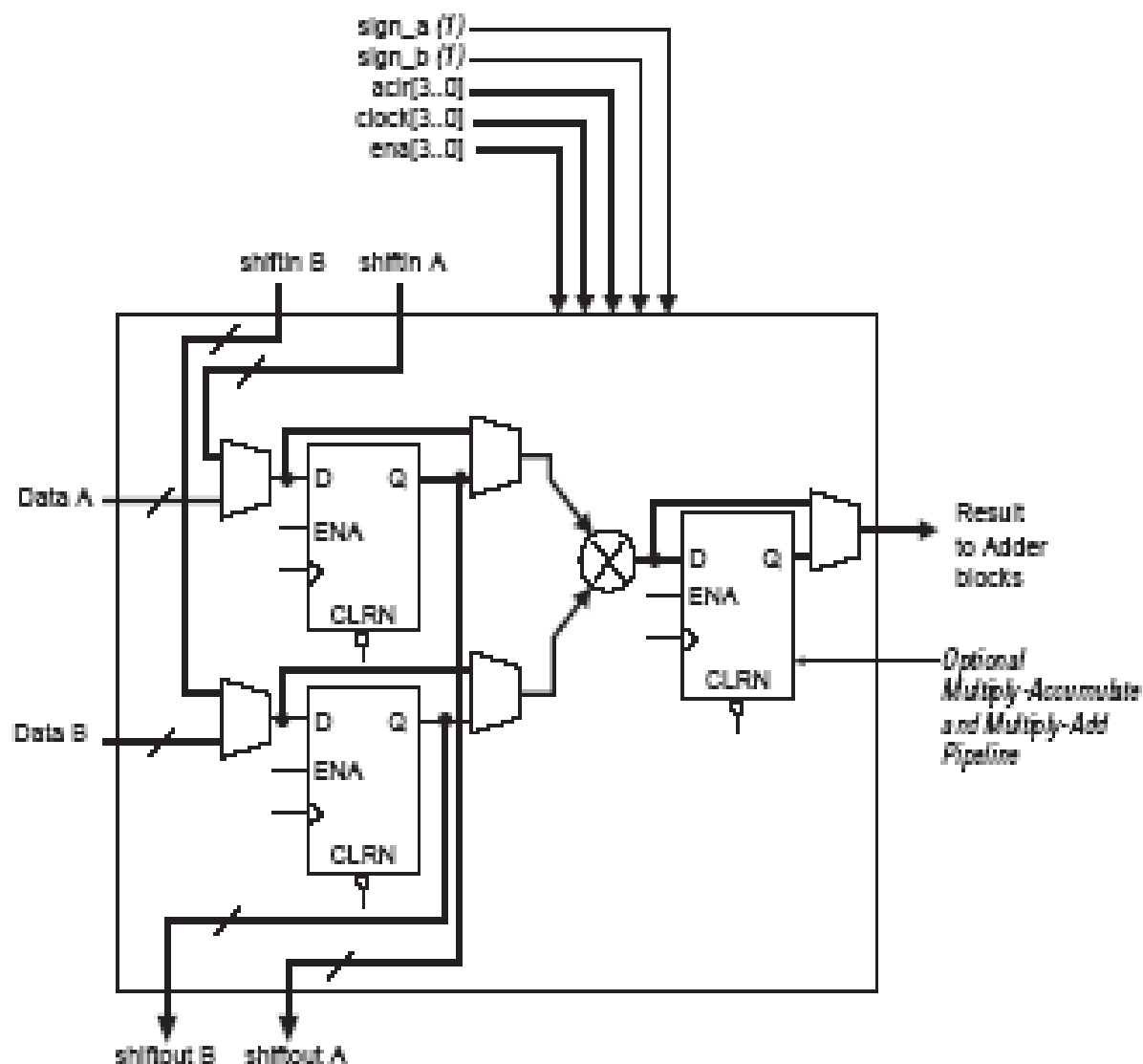


Figure 32. Multiplier Sub-Block within Stratix DSP Block



Note to Figure 32:

(1) These signals can be unregistered or registered once to match data path pipelines if required.